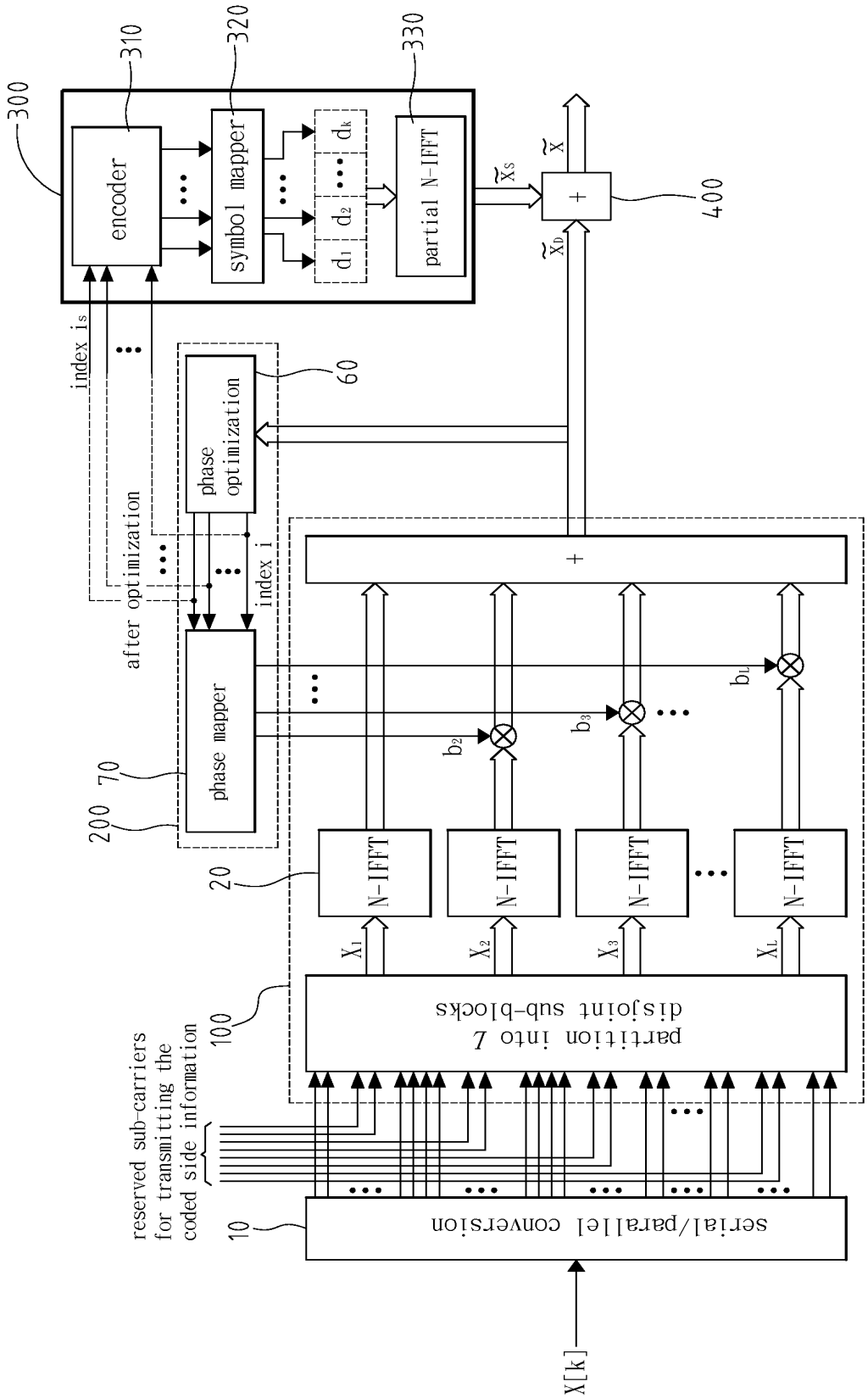
300
310
320
330
encoder
symbol mapper
partial N-IFFT
d_1 d_2 d_k
$\tilde{X}_S$
$\tilde{X}_D$
$\tilde{X}$
+
400
index i_S
60
phase optimization
after optimization
index i
200
70
phase mapper
b_2 b_3 b_L
20
N-IFFT
X_1 X_2 X_3 X_L
100
partition into L disjoint sub-blocks
reserved sub-carriers for transmitting the coded side information
10
serial/parallel conversion
X[k]

FIG. 5